

PATENT APPLICATION

Sheet 1 of 1

FORM PTO-1449 LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	ATTY. DOCKET NO. 200313784-1	APPLICATION NO.	CONFIRMATION NO.
	APPLICANT Gregg Bernard Lesartre et al.		
	FILING DATE H rewith	GROUP	

REFERENCE DESIGNATION U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
	1A	6,628,679	09/30/2003	Talarek	
	1B				
	1C				
	1D				
	1E				
	1F				
	1G				
	1H				
	1I				
	1J				
	1K				

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	Pages/Columns/Lines Where Relevant Passages/Figures Appear	Check if Translation attached
	1L					
	1M					
	1N					
	1O					
	1P					

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

	1Q	IBM Corporation, "Blue Logic Ethernet Family of Cores", IBM Microelectronics Division, (2001), (located at www.ibm.com/chips).
	1R	IBM Corporation, "Blue Logic High-Speed SERDES Family of Cores", IBM Microelectronics Division, (2003), (located at www.ibm.com/chips).
	1S	

EXAMINER /Bob Phunkulh/

DATE CONSIDERED 05/02/2008

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ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /BAP/